						Accuracy Relative to 0dB							THD	Channel	Model Designator					
			+Vcc		Dyna mic Range		T	Range	Range L/C/U Grade	Gain I	rror	Glitch	Plus	Seperation K/B/T					# of	Starting
	#	#				Step	Accu	K/B/T Grade		K/B/T			Noise		Range		Price			
							racy						@ 1KHz		0	-25	-40	-55	Pins	/100
MODEL	Bits	D/A's	+ mA	mA	dB	dB	dB	dB	dB	dB	dB	pV-sec	dB	dB	+70	+70	+85	+125		
LOG DA	\C'c																			
			~**		00.7	0.077	0.15	0.00	0.26	0.15	0.1	100	0.1		77					#10.40
AD7111	8	1	+5V	4	88.5	0.375	0.17	0>30	0>36	0.15	0.1	100	91	na	K		В	T		\$18.48
AD7111	8	1				0.75	0.35	0>42	0>48						L		С	U		\$24.26
AD7111	8	1				1.5	0.7	0>48	0>52											
AD7111	8	1				3	1.4	0>60	0>66											
AD7111	8	1				6	2.7	0>60	0>72											
AD7111A	8	1	+5V	2	88.5	0.375	0.17	0>30	0>36	0.15	0.1	10	91	na			В			\$5.34
AD7111A	8	1				0.75	0.35	0>42	0>48								С			\$7.26
AD7111A	8	1				1.5	0.7	0>48	0>52											
AD7111A	8	1				3	1.4	0>60	0>66											
AD7111A	8	1				6	2.7	0>60	0>72											
AD7112	(Dua	l versio	of AD7	111A, i	pins witl	h the A	D7528)													
AD7112A	8	2	+5V	2	88.5	0.375	0.17	0>30	0>36	0.15	0.1	10	91	87			В		20	\$6.65
AD7112A	8	2				0.75	0.35	0>42	0>48								С			\$8.50
AD7112A	8	2				1.5	0.7	0>48	0>52											
AD7112A	8	2				3	1.4	0>60	0>66											
AD7112A	8	2				6	2.7	0>60	0>72											
VOLUM	IF C) NATO	OI 7	DIT	1	'ED I		5 DIT	CHANN	IFI D	AC V	CA CI		CC						
VOLUM			<i>OL, 7</i>		VIASI	MASTER DAC, 5			CHAIVI		10, 10	JA, CI	THD				Mode	l Desig	nator	
)p	Power Supply Noise		Head	Gain	Gain	Gain	Gain Match	Plus 2nd & 3rd		Chandynami Temp Seper: Range Range				# of	Starting Price		
			Requirements		20KHz Ro			Match								Match				
	#	# +Vcc +Icc -Vee			-Iee Band		ROOM	@ @	@	@	@	@ 1KHz		Vin=+10dBu -40			-55	Pins	/100	
MODEL	Bits	D/A's	Volts	+ mA		- mA	dBu	dB	0dB	-20dB	-40dB		Av=0dB			dB	+85	+125	1 1115	7100
SSM2160	7/5	6	+5V	30	-5V	30	-100	10	.25dB	.25dB	1dB	2dB	.008%	0.02%	80		N or R		24	\$10.95
SSM2161	7/5	4	+5V	30	-5V	30	-100	10	.25dB	.25dB	1dB	2dB	.008%	0.02%	80		N or R		20	\$8.25